

PATENT *1/2/25*IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): En-Hsing Chen, et al.

Title: NAND MEMORY ARRAY INCORPORATING MULTIPLE WRITE PULSE PROGRAMMING OF INDIVIDUAL MEMORY CELLS AND METHOD FOR OPERATION OF SAME

Application No.: 10/729,844      Filed: December 5, 2003

Examiner: Mai, Son Luu      Group Art Unit: 2827

Atty. Docket No.: 023-0030      Confirmation No.: 8322

July 26, 2005

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*including Statement under 37 C.F.R. § 1.97(e)(1)*

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Pursuant to 37 C.F.R. § 1.56, § 1.97 and § 1.98, the undersigned brings to the Examiner's attention in the above-identified application the patents, publications, applications or other information identified in the attached:

- ☒ Form(s) PTO/SB/08A (1 page(s), with copy of 2 reference(s)).
- ☐ Form(s) PTO/SB/08B (N/A page(s), with copy of no reference(s)).
- ☐ Listing of Pending Applications (N/A page(s), with copy of no application(s)).
- ☒ Other: International Search Report for PCT Application No. PCT/US2004/040318 (4 pages)

Citation of such information shall not be construed as (i) an admission that the information necessarily is, or corresponds to, prior art with respect to the instant invention, (ii) a representation that a search has been made, other than as described herein, or (iii) an admission that the information cited herein is, or is considered to be, material to patentability as defined in § 1.56(b).

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Each item of information contained in this Information Disclosure Statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement.

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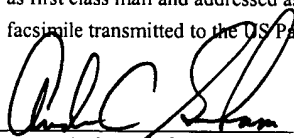
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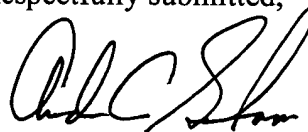
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Respectfully submitted,



Andrew C. Graham, Reg. No. 36,531  
Attorney for Applicant(s)  
(512) 338-6313 (direct)  
(512) 338-6300 (main)  
(512) 338-6301 (fax)

